

**HiPC 2018 EduHiPC Workshop**  
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**Invited Talks – National Initiatives**

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**Title: Convergence of computing: Enabling next gen developers to handle the upcoming challenges**

**Abstract**

The difference between various segments of computing like enterprise, HPC, Big Data, and analytics is becoming day by day less and less and demand for convergence and adaptable computing is increasing. In this convergence world, the new age developer needs to be able to address the development from Edge to Datacenter to be relevant to industry. However, these are today different industries and specializations combined with HW/SW, frame works, libraries, and productivity tools from many players, making the choices all the more difficult. Intel is addressing this challenge by providing the SW defined world with easy to use tool sets and eco systems to better enable development efforts. This talk is to introduce to the audience to these efforts, which will empower and enrich the productivity of the next generation of developers.

**Bio**

Seetha presently works as Director at Intel for APAC & Japan (South) for enabling HPC and AI solutions representing the Datacenter group. Has been in this role last 5 years and Architected solutions for National HPC missions in Australia, Korea, Taiwan, Japan, India and Singapore. Prior to this he led TATA CRL team building world no. 4 ranked system using X86 systems in 2007, Also provided world's first cloud HPC services through EKA. Prior to this he has played many roles as design engineer to leading the HPC solutions group at C-DAC for nearly 17+ years. Contributed to the Architecture, Design and integration of PARAM Series of HPC systems under C-DAC umbrella