

HiPC 2018 EduHiPC Workshop
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Invited Talks – National Initiatives

Ashish P. Kuvellkar (C-DAC, India)

Title: Development of HPC aware manpower under National Supercomputing Mission

Abstract

The National Supercomputing Mission is visionary program launched by the Government of India to build supercomputing capability and capacity in the country. The Mission envisages empowering academic and research institutes by installing supercomputing facilities of various sizes, networked over the National Knowledge Network (NKN). These facilities will be used to develop and run applications of national relevance. The mission includes development of highly professional High Performance Computing (HPC) aware human resource for meeting challenges of development of these applications in different domains for betterment of human lives.

The talk will include the overall plan of action for spreading awareness of HPC in different domains, among professionals in various modes of life including science, engineering, technology, arts, banking, finance, agriculture, transport and medicine. This includes conducting short-term and medium-term training programs as well as formal education programs in universities, conducting Faculty Development Programs, imparting training for domain experts for utilizing HPC towards their computing needs of numerical applications in their specific domains. Training will also be imparted to users from industry, particularly from MSME (Medium, Small and Micro Enterprises) in the abovementioned areas. Other efforts include arranging hackathons, conducting HPC awareness programs and preparing model syllabus for CS and non-CS streams and inclusion of emerging topics like Artificial Intelligence.

Bio

Mr. Ashish Kuvellkar is Senior Director (HPC Technologies) at the Centre for Development of Advanced Computing, Pune. He obtained his Master's Degree in Electrical Engineering from V J Technological Institute, Bombay University. He worked for 9 years with Hardware R&D division of Patni Computer Systems, Bombay before joining Hardware Technologies Development Group at C-DAC, Pune in 1997. At C-DAC he has been part of the team involved in development of hardware subsystems for various generations of PARAM supercomputing systems. He also teaches subjects related to Computer Architecture, Embedded Systems and Computer Networking, in various Post Graduate Diploma courses, conducted by Advanced Computing Training School of C-DAC. Currently, as a Convener of NSM Expert Group on HR Development, he is involved in coordinating the activities of the Expert Group and implementing its mandate by working with various Stakeholders from educational institutes and the industry.